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P.1

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FACSIMILE TRANSMISSION

DATE: August 2, 2004

TO: Office of Initial Patent Examination's Filing
Receipt Corrections
@ U.S. Patent and Trademark Office
Fax No.: (703) 746-9195

FROM: Frank Chau
@ F. Chau & Associates, LLC

NO. OF PAGES TO FOLLOW: 3

MESSAGE:

RE: U.S. Patent Application Serial No.: 10/780,140
METHOD FOR OPTIMIZATION OF LOGIC CIRCUITS
FOR ROUTABILITY
Group Art Unit: 2816
Our Docket No.: YOR920030437US1 (8728-653)

Dear Sir/Madam:

Attached hereto, please find a Request for a Corrected Filing Receipt in connection with the above-identified patent application.

Upon review and acceptance, please provide us with an updated Corrected Filing Receipt.

Thank you,

Frank Chau

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Thank you.

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Dougherty, et al.

Group Art Unit: 2816

Serial No: 10/780,140

Docket: YOR920030437US1 (8728-653)

Filed: February 17, 2004

For: **METHOD FOR OPTIMIZATION OF LOGIC CIRCUITS FOR ROUTABILITY**

Office of Initial Patent Examination
Filing Receipt Corrections
PO Box 1450
Alexandria VA 22313

REQUEST FOR A CORRECTED FILING RECEIPT


Sir:

It is respectfully requested that the Filing Receipt attached be corrected by changing the residence of the inventor, Prabhakar N. Kudva, from "Ney York, NY" to -New York, New York- as indicated in the Declaration document filed on February 17, 2004. A copy of the Declaration and a copy of the Filing Receipt which was mailed by the U.S. Patent and Trademark Office is attached with the correction indicated in red ink.

In view of this Patent Office error, no fee is due for this correction

Respectfully submitted,

By:


Frank Chau
Reg. No. 34,136
Attorney for Applicant

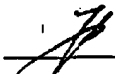
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Dated: 


Frank Chau



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10/780,140	02/17/2004	2816	900	YOR920030437US1 (8728-653	10	14	3

Frank Chau, Esq.
F. CHAU & ASSOCIATES, LLC
1900 Hempstead Turnpike
East Meadow, NY 11554

CONFIRMATION NO. 9678

UPDATED FILING RECEIPT



OC000000013076156

Date Mailed: 06/25/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

William E. Dougherty JR., Pleasant Valley, NY;
Victor Kravets, White Plains, NY;
Prabhakar N. Kudva, ~~New~~ York, NY; **New**
Andrew J. Sullivan, Wappinger Falls, NY;

Assignment For Published Patent Application

International Business Machines Corporation;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 05/10/2004

Projected Publication Date: 08/18/2005

Non-Publication Request: No

Early Publication Request: No

Title

Method for optimization of logic circuits for routability

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Preliminary Class

327

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